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**INFORMATION DISCLOSURE STATEMENT BY APPLICANT**  
(Use Several Sheets if Necessary)

(37 CFR § 1.98(b))

Applicant: Gheorghe Stefan et al.

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Group Art Unit: 2181

## U.S. PATENT DOCUMENTS

Examiner Initials		Patent Number/ Publication Number	Issue Date/ Publication Date	Applicant / Patentee	Class	Subclass	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
	AO						

## FOREIGN PATENTS OR PUBLISHED FOREIGN PATENT APPLICATIONS

		Document Number	Publication Date	Country / Patent Office	Class	Subclass	Translation	
							Yes	No
	AP							
	AQ							
	AR							
	AS							
	AT							

## OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)

	AU	Yamashita et al., "A 3.84 GIPS Integrated Memory Array Processing with 64 Processing Elements and a 2-Mb SRAM," IEEE Journal of Solid-State Circuits, Vol. 29, No. 11, November 1994
	AV	
	AW	
	AX	
	AY	
	AZ	
	BA	
	BB	
	BC	

Examiner:

Date Considered:

**EXAMINER:**

Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.